

2812

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TI-32535

Craig T. Salling, et al.

Art Unit: 2812

Serial No: 10/051,962

Examiner: TBD

Filed: 1/16/02

For: Eliminating Substrate Noise by an Electrically Isolated High-Voltage I/O Transistor

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

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Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith.

Respectfully submitted,

Gary C. Honeycutt

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Attorney for Applicants
Reg. No. 20,250

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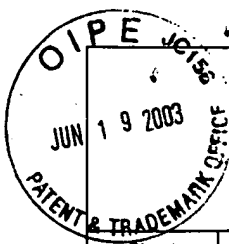


PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete If Known		
				Application Number	10/051,962	
Sheet		1	of	1	Attorney Docket No.	TI-32535
		Filing Date	1/16/02			
		First Named Inventor	Craig T. Salling, et al.			
		Group Art Unit	2812			
		Examiner Name	TBD			

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	AA	4,994,888		Taguchi et al.	2/19/91	
	AB	5,218,228		Williams et al.	6/8/93	
	AC	5,489,540		Liu et al.	2/6/96	
	AD	5,340,762		Vora	8/23/94	
	AE	5,583,061		Williams et al.	12/10/96	
	AF	5,851,864		Ito et al.	12/22/98	
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FOREIGN PATENT DOCUMENTS								
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		Office ³	Number ⁴	Kind Code ² (if known)				
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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